

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	ON CHIP RESISTOR CALIBRATION STRUCTURE AND METHOD	
<p>Application Number : Date : First Named Applicant: Terence B. Hook Confirmation Number: Attorney Docket Number: BUR920030074US1</p>		
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Submitted By:	Elec. Sign.	Sign. Capacity
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Documents being submitted:	Files
us-assignment	BUR920030074US1-usassn.xml us-assignment.xsl us-assignment.dtd BUR920030074assign1.tif BUR920030074assign2.tif
us-request	BUR920030074US1-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	BUR920030074US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	BUR920030074dec1.tif
us-declaration	BUR920030074dec2.tif
application-body	BUR920030074spec-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfr.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblk.dtd BUR920030074US1FIGdrawing1.tif BUR920030074US1FIGdrawing2.tif BUR920030074US1FIGdrawing3.tif BUR920030074US1FIGdrawing4.tif
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